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			Application Number		
			Filing Date	November 28, 2003	
			First Named Inventor	YUUKI, FUMIO	
			Art Unit		
Examiner Name					
Sheet	1	of	1	Attorney Docket Number	520.43305X00

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/LW/		2000 IEEE International Solid-State Circuits Conference TP 15.7 A 20 Gb/s CMOS Multi-Channel Transmitter and Receiver Chip Set for Ultra-High Resolution Digital Display.	
/LW/		2001 IEEE International Solid-State Circuits Conference 4.4 " 5Gb/s Bidirectional Balanced-Line Link Complaint With Plesiochronous Clocking"	

Examiner Signature	/Lawrence Williams/ (03/29/2007)	Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

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